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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application / Conf. No.	10/600,848 / 7556
	Filing Date	June 19, 2003
	First Named Inventor	Jonathan B. Ballagh
	Examiner Name	Andre Pierre Louis
	Art Unit	2123
	Patent No.	
Mail Stop: AMENDMENT	Attorney Docket Number	X-1192 US
Express Mail Receipt No.		
Total Number of Pages in This Submission		

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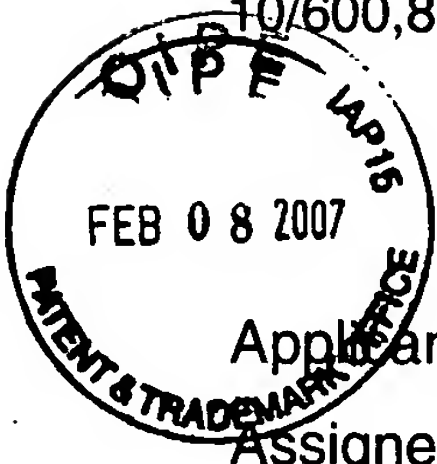
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X-1192 US
10/600,848

PATENT
Conf. No.: 7556



IN THE UNITED STATES PATENT OFFICE

Applicants: Jonathan B. Ballagh et al.
Assignee: Xilinx, Inc.
Title: "Clock Stabilization Detection for Hardware Simulation"
Serial No.: 10/600,848 Filed: 06-19-2003
Examiner: Andre Pierre Louis Art Unit: 2123
Docket No.: X-1192 US Conf. No.: 7556

Mail Stop AMENDMENT
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RESPONSE TO SECOND OFFICE ACTION

Dear Sir:

In response to the Second Office Action mailed from the Patent Office on November 17, 2006, please consider the following remarks.

Listing of the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.